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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/797,647	03/10/2004	Michael W. Morrison	2269-6103US (03-0954.00/U)	4195
24247	7590	09/29/2005	EXAMINER	
TRASK BRITT P.O. BOX 2550 SALT LAKE CITY, UT 84110			LEWIS, MONICA	
			ART UNIT	PAPER NUMBER
			2822	
DATE MAILED: 09/29/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

AKC

Office Action Summary

Application No.

10/797,647

Applicant(s)

MORRISON ET AL.

Examiner

Monica Lewis

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 30days MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-36 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☒ Claim(s) 1-36 are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This action is in response to the application filed March 10, 2004.

Election/Restrictions

2. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1-27, drawn to a semiconductor device comprising a BGA package for a segmented voltage plane, classified in class 257, subclass 676.
 - II. Claims 28-36, drawn to a process for fabricating a BGA package for a segmented voltage plane, classified in class 438, subclass 106.

Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). The product can be made by the following methods: a) selective deposition could be utilized instead of etching.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

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Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

3. If the Applicant selects Invention I disclosed then application contains claims directed to the following patentably distinct species of the claimed invention:

Embodiment I (Claims 1 and 2), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film, isolation slot, a plurality of electrical conductors and a plurality of discrete conductive elements disposed upon the surface of the dielectric film;

Embodiment II (Claims 1, 3-6 and 13-15), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film wherein the conductive layer is configured to operably coupled to a voltage source;

Embodiment III (Claims 1 and 7), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film, isolation slot that substantially segments the electrically conductive layer;

Embodiment IV (Claims 1 and 8-11), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film, isolation slot and longitudinal slots formed through the conductive layer and the dielectric film;

Embodiment V (Claims 1 and 8-11), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film, isolation slot and longitudinal slots formed through the conductive layer and the dielectric film;

Embodiment VI (Claims 1 and 12), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film wherein the dielectric film comprises a multilayer film having at least one additional layer of conductive traces, at least one additional layer dielectric layer and at least one interlayer conductive via effecting connection between the at least one conductive trace and a conductive trace of the at least one additional layer of conductive traces;

Embodiment VII (Claims 16 and 17), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a top surface, a via, a conductive layer adhered to the dielectric

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film, isolation slot, a plurality of electrical conductors and a plurality of discrete conductive elements disposed upon the surface of the dielectric film;

Embodiment VIII (Claims 16 and 18-21), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a top surface, a via, a conductive layer adhered to the dielectric film wherein the conductive layer is configured to operably coupled to a voltage source;

Embodiment IX (Claims 16 and 22), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film, isolation slot that substantially segments the electrically conductive layer; and

Embodiment X (Claims 16 and 23-26), directed to a semiconductor device comprising a die, bond pads, a dielectric film having at least one conductive trace disposed upon a surface, a via, a conductive layer disposed between the active surface of the die and the dielectric film, isolation slot and longitudinal slots formed through the conductive layer and the dielectric film.

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, none are generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable

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thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Monica Lewis whose telephone number is 571-272-1838. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300 for regular and after final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

ML

September 27, 2005

A handwritten signature in black ink, followed by the number 2822 written in a similar cursive style.